

```
B2:
39 StartOSR === 39 1 [39 38 40 41 42 120 123 0 95 104 90] #0:control, 1:abiO, 2:memory, 3:rawptr:BotPTR, 4:return_address, 5:rawptr:BotPTR Type:(0:control, 1:abiO, 2:memory, 3:rawptr:BotPTR, 4:return_address, 5:rawptr:BotPTR)
38 MachProj === 39 [36 77 105 111] #0/Unmatched Type:control !jvms: Store:main @ bci:39 (line 15)
40 MachProj === 39 [36 77 92 99 105 107 111 121] #2/Unmatched Memory: @BotPTR *+bot, idx=bot; !jvms: Store:main @ bci:39 (line 15)
41 MachProj === 39 [36 2] #3 Type:rawptr:BotPTR !jvms: Store:main @ bci:39 (line 15)
42 MachProj === 39 [36 77 105 111] #5 Type:rawptr:BotPTR !jvms: Store:main @ bci:39 (line 15)
120 MachProj === 39 [2] #1/Unmatched Type:abiO !jvms: Store:main @ bci:39 (line 15)
123 MachProj === 39 [2] #4 Type:return_address !jvms: Store:main @ bci:39 (line 15)
0 Con === 39 [] Type:top
77 loadI === 38 40 42 [76 36] #16/0x0000000000000010 Type:int !jvms: Store:main @ bci:39 (line 15)
105 loadI === 38 40 42 [156 49 51 26 9 36] #24/0x0000000000000018 Type:int !jvms: Store:main @ bci:39 (line 15)
111 loadI === 38 40 42 [110 36] #8/0x0000000000000008 Type:int !jvms: Store:main @ bci:39 (line 15)
36 CallLeafDirect === 38 0 40 41 0 42 1 111 105 77 [37 35 93 168] OSR_migration_end # void ( rawptr:BotPTR ) Type:(0:control, 1:abiO, 2:memory, 3:rawptr:BotPTR, 4:return_address) !jvms: Store:main @ bci:39 (line 15)
37 MachProj === 36 [] #1D005/Eat Type:bottom
35 MachProj === 36 [146] #0/Unmatched Type:control !jvms: Store:main @ bci:39 (line 15)
93 MachProj === 36 [92 57 107 32 121] #2/Unmatched Memory: @rawptr:BotPTR, idx=Raw; !jvms: Store:main @ bci:39 (line 15)
168 MachProj === 36 () #6/Eat Type:bottom
90 loadConI0 === 39 [91 85 112 114 113] #0/0x00000000 Type:int:0
91 MachProj === 90 [] #1 Type:int:-1..1
95 loadComp === 39 [97 101 94] java/lang/Classjava.lang.Class
104 tsiLoad === 39 [57 32] Type:rawptr:BotPTR !jvms: Store:main @ bci:48 (line 16)
146 jmpDir === 35 [34] Type:bottom lorig=127
```

```
B3:
34 Loop === 34 146 145 [34 76 99 31 32 110] Type:control !jvms: Store:main @ bci:39 (line 15)
76 Phi === 34 77 78 [75 31 21] Type:int !jvms: Store:main @ bci:39 (line 15)
99 Phi === 34 40 100 [98 107] Memory: @java/lang/Classjava.lang.Class
110 Phi === 34 111 112 [108] Type:int !jvms: Store:main @ bci:39 (line 15)
107 MergeMem === 0 40 93 99 [31] ( N93:rawptr:BotPTR N99:java/lang/Classjava.lang.Class
32 loadP === 34 93 104 [31] rawptr:BotPTR Type:rawptr:BotPTR
108 incl_rReg === _ 110 [109 114 31] #1/0x00000001 Type:int !jvms: Store:main @ bci:51 (line 18)
109 MachProj === 108 [] #1 Type:int:-1..1
31 safePoint_poll_tls === 34 0 107 0 0 32 0 105 76 108 0 [33 144] Type:(0:control, 1:abiO, 2:memory, 3:rawptr:BotPTR, 4:return_address) !jvms: Store:main @ bci:54 (line 19)
33 MachProj === 31 [] #1 Type:abiO
144 jmpDir === 31 [30] Type:bottom lorig=127
```

```
B4:
30 CountedLoop === 30 144 143 [30 75 98 114 28] inner pre of N246 Type:control !jvms: Store:main @ bci:18 (line 9)
75 Phi === 30 76 73 [84 73] Type:int !jvms: Store:main @ bci:18 (line 9)
98 Phi === 30 99 96 [97 96] Memory: @java/lang/Classjava.lang.Class
114 Phi === 30 108 90 [113 29] Type:int !jvms: Store:main @ bci:18 (line 9)
29 compl_rReg_imm === _ 114 [28] #100/0x00000064 Type:int:-1..1
28 jmpCon === 30 29 [27 116] P=0,990243, C=3792,000000 Type:(0:control, 1:control) !jvms: Store:main @ bci:21 (line 9)
27 IfTrue === 28 [142] #1 Type:control !jvms: Store:main @ bci:21 (line 9)
116 IfFalse === 28 [164] #0 Type:control !jvms: Store:main @ bci:21 (line 9)
```

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B5:
142 Region === 142 27 [142 25 97] Type:control
97 storeImmI === 142 98 95 [103 96] memory Memory: @java/lang/Classjava.lang.Class
26 testI_reg === _ 105 [25] #0/0x00000000 Type:int:-1..1
25 jmpCon === 142 26 [46 24] P=0,000000, C=3755,000000 Type:(0:control, 1:control) !jvms: Store:main @ bci:30 (line 11)
46 IfFalse === 25 [152] #0 Type:control !jvms: Store:main @ bci:30 (line 11)
24 IfTrue === 25 [141] #1 Type:control !jvms: Store:main @ bci:30 (line 11)
```

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B51:
141 Region === 141 24 [141 140] Type:control
140 jmpDir === 141 [23] Type:bottom lorig=127
```

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B6:
23 Region === 23 140 139 [23 96 19] Type:control !jvms: Store:main @ bci:57 (line 20)
96 Phi === 23 97 98 [102 98 94] Memory: @java/lang/Classjava.lang.Class
21 incl_rReg === _ 76 [22 20] #1/0x00000001 Type:int
22 MachProj === 21 [] #1 Type:int:-1..1
73 incl_rReg === _ 75 [74 81 75 72 20 17] #1/0x00000001 Type:int !jvms: Store:main @ bci:57 (line 20)
74 MachProj === 73 [] #1 Type:int:-1..1
20 compl_rReg === _ 73 21 [18] Type:int:-1..1
19 jmpLoopEnd === 23 20 [115 18] P=0,900000, C=1,000000 Type:(0:control, 1:control) !jvms: Store:main @ bci:13 (line 7)
115 IfTrue === 19 [163] #1 Type:control !jvms: Store:main @ bci:13 (line 7)
18 IfFalse === 19 [138] #0 Type:control
```

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B52:
152 Region === 152 46 [152 151] Type:control
151 jmpDir === 152 [45] Type:bottom lorig=127
```

```
B7:
138 Region === 138 18 [138 16] Type:control
17 compl_rReg_imm === _ 73 [16] #99/0x00000063 Type:int:-1..1
16 jmpCon === 138 17 [52 15] P=0,999999, C=-1,000000 Type:(0:control, 1:control)
52 IfTrue === 16 [158] #1 Type:control
15 IfFalse === 16 [137] #0 Type:control !jvms: Store:main @ bci:13 (line 7)
```

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B29:
163 Region === 163 115 [163 143] Type:control
143 jmpDir === 163 [30] Type:bottom lorig=127
```

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B19:
158 Region === 158 52 [158 157 94 72] Type:control
94 storeImmI === 158 96 95 [102 103 92] memory Memory: @java/lang/Classjava.lang.Class
72 castI1 === 158 73 [71] Type:int
157 jmpDir === 158 [51] Type:bottom lorig=127
```

```
B8:
137 Region === 137 15 [137 136] Type:control
136 jmpDir === 137 [14] Type:bottom lorig=127
```

```
B20:
51 OuterStripMinedLoop === 51 157 156 [51 71 155] Type:control
71 Phi === 51 72 68 [70 86 87 62] Type:int:<max>!www !jvms: Store:main @ bci:18 (line 9)
155 jmpDir === 51 [50] Type:bottom lorig=127
```

```
B21:
50 CountedLoop === 50 155 154 [50 70 48] inner main of N50 strip mined Type:control !jvms: Store:main @ bci:18 (line 9)
70 Phi === 50 71 68 [68 68] Type:int:<max>!www !jvms: Store:main @ bci:18 (line 9)
49 testI_reg === _ 105 [48] #0/0x00000000 Type:int:-1..1
48 jmpCon === 50 49 [47 47] P=0,000000, C=3755,000000 Type:(0:control, 1:control) !jvms: Store:main @ bci:30 (line 11)
67 IfTrue === 48 [161] #1 Type:control !jvms: Store:main @ bci:30 (line 11)
47 IfFalse === 48 [153] #0 Type:control !jvms: Store:main @ bci:30 (line 11)
```

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B22:
161 Region === 161 67 [161 60] Type:control
65 loadConI === 1 [63] #2000/0x000007d0 Type:int:2000
88 loadConI === 1 [67] #99/0x00000063 Type:int:99
87 subI_rReg === _ 88 71 [89 85] Type:int
89 MachProj === 87 [] #1 Type:int:-1..1
86 compl_rReg_imm === _ 71 [85] #99/0x00000063 Type:int:-1..1
85 cmovI_reg === _ 86 87 90 [64 63] g Type:int
64 compl_rReg_imm === _ 85 [63] #2000/0x000007d0 Type:int:-1..1
63 cmovI_regU === _ 64 85 65 [62] nbe Type:int
62 addI_rReg === _ 63 71 [66 61] Type:int
66 MachProj === 62 [] #1 Type:int:-1..1
68 addI_rReg_imm === _ 70 [69 81 71 70 61 56 55] #2/0x00000002 Type:int !jvms: Store:main @ bci:57 (line 20)
69 MachProj === 68 [] #1 Type:int:-1..1
61 compl_rReg === _ 68 62 [60] Type:int:-1..1
60 jmpLoopEnd === 161 61 [59 106] P=0,900000, C=-1,000000 Type:(0:control, 1:control) !jvms: Store:main @ bci:13 (line 7)
59 IfFalse === 60 [160] #0 Type:control !jvms: Store:main @ bci:13 (line 7)
106 IfTrue === 60 [162] #1 Type:control !jvms: Store:main @ bci:13 (line 7)
```

```
B27:
153 Region === 153 47 [153 150] Type:control
150 jmpDir === 153 [45] Type:bottom lorig=127
```

```
B28:
45 Region === 45 151 150 [45 84 103 147 113] Type:control
84 Phi === 45 75 70 [78] Type:int
103 Phi === 45 97 94 [100] Memory: @java/lang/Classjava.lang.Class
113 Phi === 45 114 90 [112] Type:int
147 jmpDir === 45 [43] Type:bottom lorig=127
```

```
B26:
162 Region === 162 106 [162 154] Type:control
154 jmpDir === 162 [50] Type:bottom lorig=127
```

```
B23:
160 Region === 160 59 [160 56 57] Type:control
92 MergeMem === 0 40 93 94 [56] ( N93:rawptr:BotPTR N94:java/lang/Classjava.lang.Class
87 loadP === 160 93 104 [56] rawptr:BotPTR Type:rawptr:BotPTR
56 safePoint_poll_tls === 160 0 92 0 0 57 0 105 68 0 0 [58 54] Type:(0:control, 1:abiO, 2:memory, 3:rawptr:BotPTR, 4:return_address) !jvms: Store:main @ bci:60 (line 21)
58 MachProj === 56 [] #1 Type:abiO
55 compl_rReg_imm === _ 68 [54] #99/0x00000063 Type:int:-1..1
54 jmpCon === 56 55 [53 117] P=0,900000, C=-1,000000 Type:(0:control, 1:control)
53 IfTrue === 54 [159] #1 Type:control
117 IfFalse === 54 [165] #0 Type:control
```

```
B25:
159 Region === 159 53 [159 156] Type:control
156 jmpDir === 159 [51] Type:bottom lorig=127
```

```
B24:
165 Region === 165 117 [165 135] Type:control
135 jmpDir === 165 [14] Type:bottom lorig=127
```

```
B9:
14 Region === 14 136 135 [14 81 102 12] Type:control
81 Phi === 14 73 68 [80 13] Type:int
102 Phi === 14 96 94 [101 122] Memory: @java/lang/Classjava.lang.Class
13 compl_rReg_imm === 81 [122] #100/0x00000064 Type:int:-1..1
12 jmpCon === 14 13 [11 119] P=0,500000, C=-1,000000 Type:(0:control, 1:control)
11 IfTrue === 12 [134] #1 Type:control
119 IfFalse === 12 [167] #0 Type:control !jvms: Store:main @ bci:13 (line 7)
```

```
B12:
134 Region === 134 11 [134 133 101 80] Type:control
101 storeImmI === 134 102 95 [100 122] memory Memory: @java/lang/Classjava.lang.Class
80 castI1 === 134 81 [79] Type:int
133 jmpDir === 134 [110] Type:bottom lorig=127
```

```
B13:
10 CountedLoop === 10 133 132 [10 79 8] inner post of N246 Type:control !jvms: Store:main @ bci:18 (line 9)
79 Phi === 10 80 82 [82 78] Type:int:<100>www !jvms: Store:main @ bci:18 (line 9)
9 testI_reg === _ 105 [8] #0/0x00000000 Type:int:-1..1
8 jmpCon === 10 9 [44 7] P=0,000000, C=3755,000000 Type:(0:control, 1:control) !jvms: Store:main @ bci:30 (line 11)
44 IfFalse === 8 [149] #0 Type:control !jvms: Store:main @ bci:30 (line 11)
7 IfTrue === 8 [131] #1 Type:control !jvms: Store:main @ bci:30 (line 11)
```

```
B10:
167 Region === 167 119 [167 128] Type:control
128 jmpDir === 167 [3] Type:bottom lorig=127
```

```
B17:
149 Region === 149 44 [149 148] Type:control
148 jmpDir === 149 [43] Type:bottom lorig=127
```

```
B14:
131 Region === 131 7 [131 5] Type:control
82 incl_rReg === _ 79 [83 79 6] #1/0x00000001 Type:int !jvms: Store:main @ bci:57 (line 20)
83 MachProj === 82 [] #1 Type:int:-1..1
6 compl_rReg_imm === _ 82 [5] #100/0x00000064 Type:int:-1..1
5 jmpLoopEnd === 131 6 [118 4] P=0,900000, C=-1,000000 Type:(0:control, 1:control) !jvms: Store:main @ bci:13 (line 7)
118 IfTrue === 5 [166] #1 Type:control !jvms: Store:main @ bci:13 (line 7)
4 IfFalse === 5 [130] #0 Type:control !jvms: Store:main @ bci:13 (line 7)
```

```
B15:
130 Region === 130 4 [130 129] Type:control
129 jmpDir === 130 [3] Type:bottom lorig=127
```

```
B16:
166 Region === 166 118 [166 132] Type:control
132 jmpDir === 166 [110] Type:bottom lorig=127
```

```
B18:
43 Region === 43 148 147 [43 78 100 145 112] Type:control
78 Phi === 43 79 84 [76] Type:int
100 Phi === 43 101 103 [99] Memory: @java/lang/Classjava.lang.Class
112 Phi === 43 90 113 [110] Type:int
145 jmpDir === 43 [34] Type:bottom lorig=127
```

```
B11:
3 Region === 3 129 128 [3 2 122] Type:control
122 Phi === 3 101 102 [121] Memory: @java/lang/Classjava.lang.Class
121 MergeMem === 0 40 93 122 [2] ( N93:rawptr:BotPTR N122:java/lang/Classjava.lang.Class
2 Ret === 3 120 121 41 123 [1] Type:bottom
```